

STATE BOARD OF TECHNICAL EDUCATION AND TRAINING
BOARD OF EXAMINATIONS, CHENNAI 600 025.

TIME TABLE – OCTOBER 2014

DIPLOMA IN ELECTRONICS AND COMMUNICATION ENGINEERING (1040)

Date and Day	Time	Subject Code	Subjects as per the Scheme of Examination	Code No.
<i>(L-Scheme)</i>				
III SEMESTER				
24.10.2014 Friday	9.30 am – 12.30 pm	24031	Electronic Devices and Circuits	
27.10.2014 Monday	9.30 am – 12.30 pm	25233	'C' Programming #	
29.10.2014 Wednesday	9.30 am – 12.30 pm	24032	Electrical Circuits and Instrumentation	
IV SEMESTER				
29.10.2014 Wednesday	2.15 pm – 5.15 pm	24041	Industrial Electronics	
31.10.2014 Friday	2.15 pm – 5.15 pm	24042	Communication Engineering	
3.11.2014 Monday	2.15 pm – 5.15 pm	24043	Digital Electronics	
7.11.2014 Friday	2.15 pm – 5.15 pm	24044	Linear Integrated Circuits	
V SEMESTER				
25.10.2014 Saturday	9.30 am – 12.30 pm	24051	Advanced Communication Systems	
28.10.2014 Tuesday	9.30 am – 12.30 pm	24052	Microcontroller	
30.10.2014 Thursday	9.30 am – 12.30 pm	24053	Very Large Scale Integration	
6.11.2014 Thursday	9.30 am – 12.30 pm	24071	Elective Theory – I Digital Communication	
		23072	Programmable Logic Controller @	
VI SEMESTER				
24.10.2014 Friday	2.15 pm – 5.15 pm	24061	Embedded Systems	
25.10.2014 Saturday	2.15 pm – 5.15 pm	24081	Elective Theory-II Television Engineering	
		23082	Biomedical Instrumentation @	
		24083	Electronic System Design	
27.10.2014 Monday	2.15 pm – 5.15 pm	24682	Computer Hardware and Networks \$	
<i>(K-Scheme)</i>				
V SEMESTER				
28.10.2014 Tuesday	2.15 pm – 5.15 pm	24763	Elective Theory – I Robotics and Auto Electronics **	
		14071	Digital Signal Processing	
VI SEMESTER				
6.11.2014 Thursday	9.30 am – 12.30 pm	14081	Elective Theory – II Digital Image Processing	

Common with Computer Engineering (L – Scheme)
\$ Common with Information Tech (L-Scheme)

@ Common with EEE (L-Scheme)
** Common with Electronics (R) (L-Scheme)